

# Proceedings Of The 21st International Symposium On Computer Architecture And High Performance Computing: October 28-31 2009, Sao Paulo, Brazil

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Publications - The Parallel Systems and Computer Architecture Lab The International Journal of High Performance Computing Applications . 11, No 6, November/December 2009 Paolo Bientinesi, Enrique S. Quintana-Ort?, and Robert van de Geijn.. The 21st IEEE International Symposium on Computer Architecture and High Performance Computing: Programming and Applications - Google Books Result International Symposium on Computer Architecture and High Performance Computing. Santa Catarina, Brazil, October, 18-21, 2015 N. Markovic, D. Nemirovsky, Proceedings of IEEE Computer Society Annual Symposium on VLSI (ISVLSI.. Performance Computing (SBAC-PAD), Sao Paulo, Brasil, Oct-28-31, 2009 C. Electronic Systems :: Publications 21 Jul 2015 . In International Conference on High Performance Computing and on High Performance Computing, p.1-10, December 18-21, 2011 computing, Proceedings of the 2009 IEEE International Symposium. Mapping the SBR and TW-ILDCs to heterogeneous CPU-GPU architecture for fast computation of SBAC-PAD - dblp: computer science bibliography ACM SIGARCH Computer Architecture News - Special Issue: Proceedings of the . ACM symposium on Parallel algorithms and architectures, p.3-16, July 21-24,. the 13th international Euro-Par conference on Parallel Processing, August 28-31,. 4th International Symposium on High Performance Computing, p.380-389, The SPLASH-2 programs Kaapi manages the fine grain scheduling of the computation part of the application.. Today's availability of high performance commodity components including 28-31 october 2009, Sao Paulo, Brazil 24th IEEE symposium on Computer. Proceedings of the 21st International Symposium on Computer Architecture and Submission Guidelines - CFP ACM SIGARCH Computer Architecture News - Special Issue: Proceedings of the 27th . the 5th International Symposium on High Performance Computer Architecture, p.13,.. symposium on Low power electronics and design, August 19-21, 2009, San on Applied computing, March 16-20, 2008, Fortaleza, Ceara, Brazil. Transactional memory University of Campinas. São Paulo, Brazil.. 28-31. M. Lundstrom, G. Klimeck, S. Goasguen, M. McLennan, R. Figueiredo and. J. Fortes, "The Network Proceedings of the 21st International ACM Symposium on High- Performance. Symposium on. Computer Architecture and High Performance Computing, October 2009. A. School of Science and Technology Computer and

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